

Fig. 1 PRIOR ART

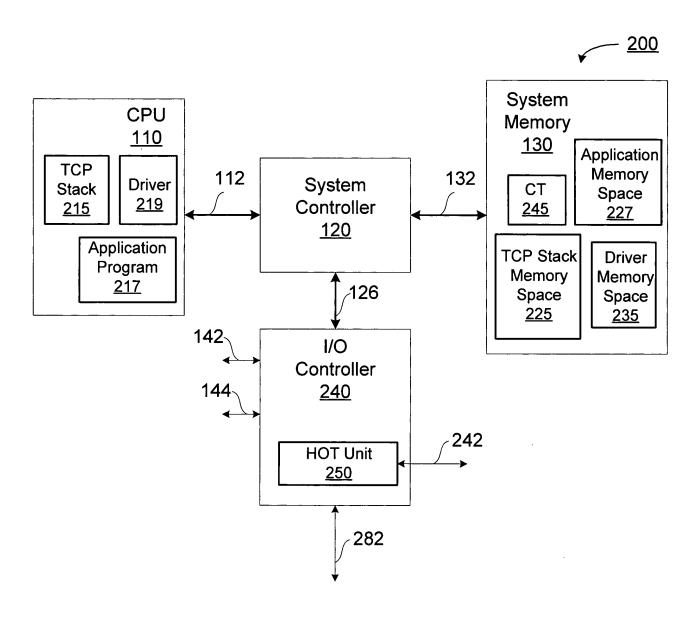


Fig. 2A

+

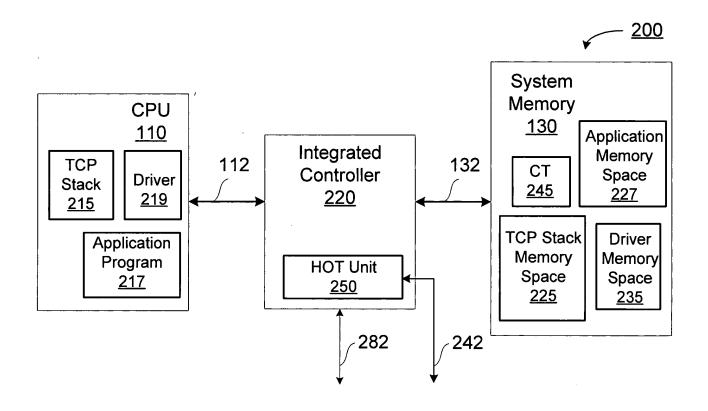


Fig. 2B

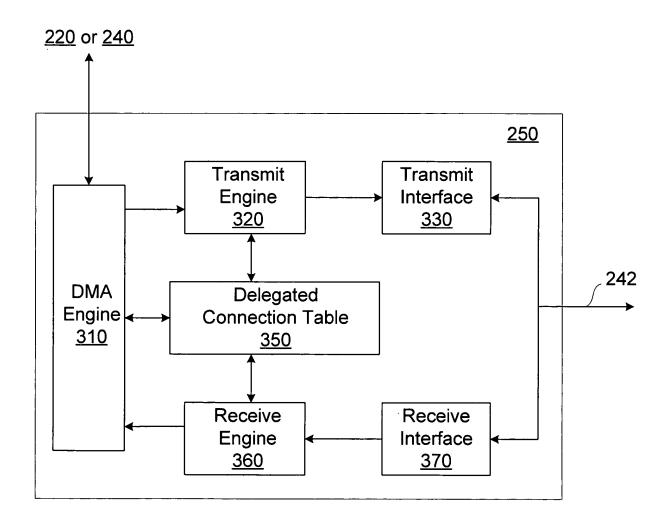


Fig. 3

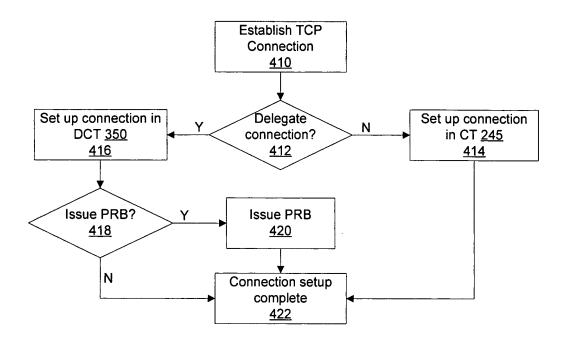


Fig. 4A

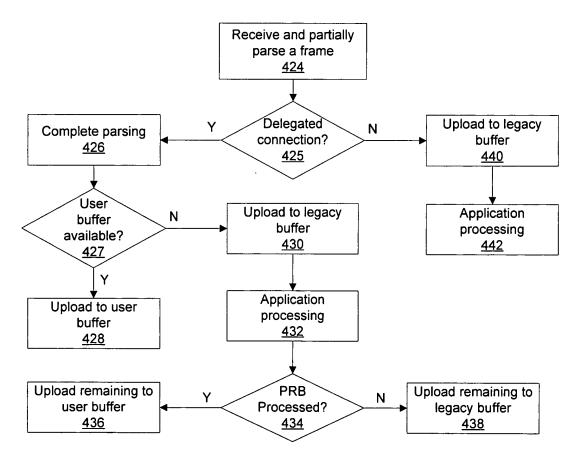


Fig. 4B

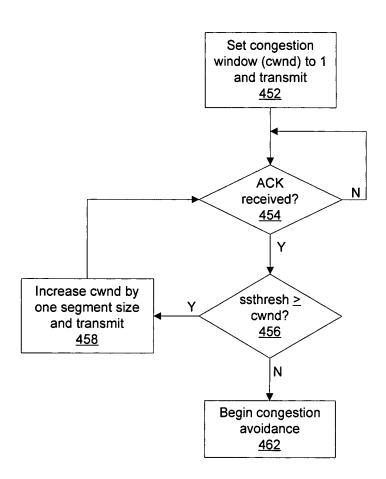


Fig. 4C

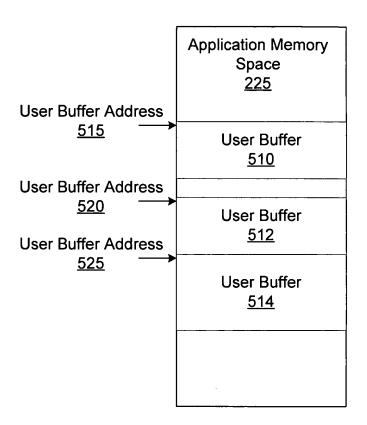
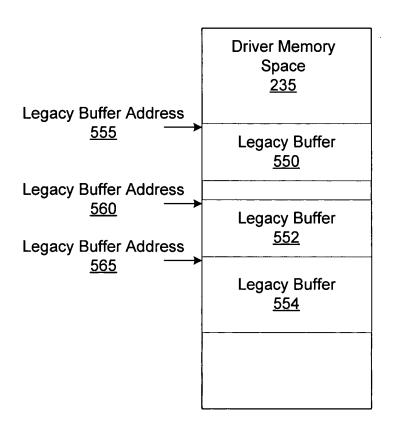


Fig. 5A

User Buffer Address <u>515</u> User Buffer Length <u>535</u>
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Fig. 5B



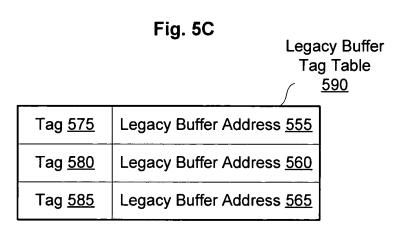


Fig. 5D

Legacy Buffer Address <u>555</u>	Legacy Buffer Length <u>570</u>	Tag <u>575</u>
		i

Fig. 5E

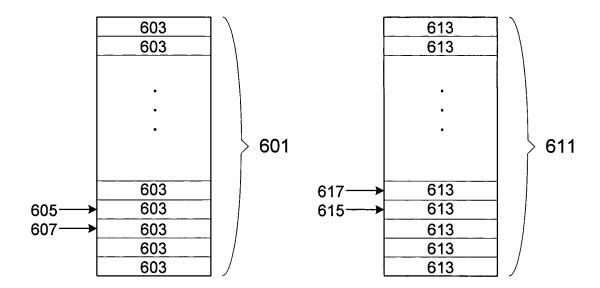


Fig. 6A Fig. 6B

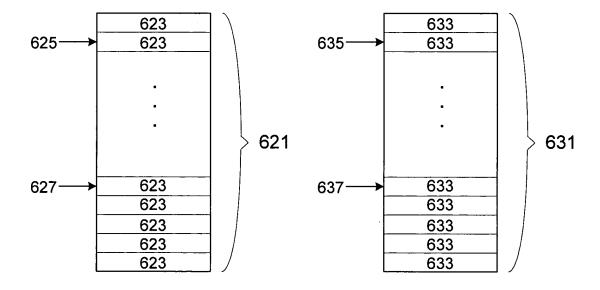


Fig. 6C Fig. 6D

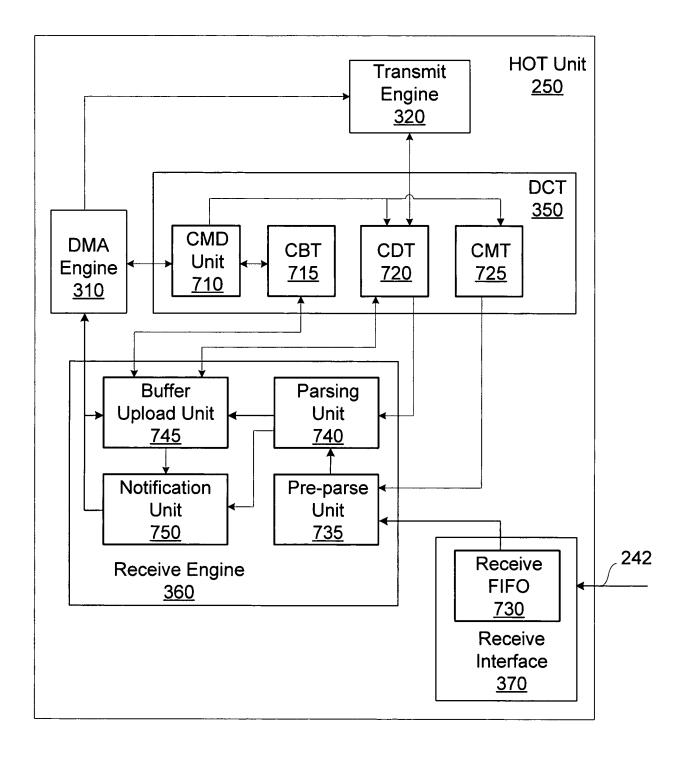


Fig. 7

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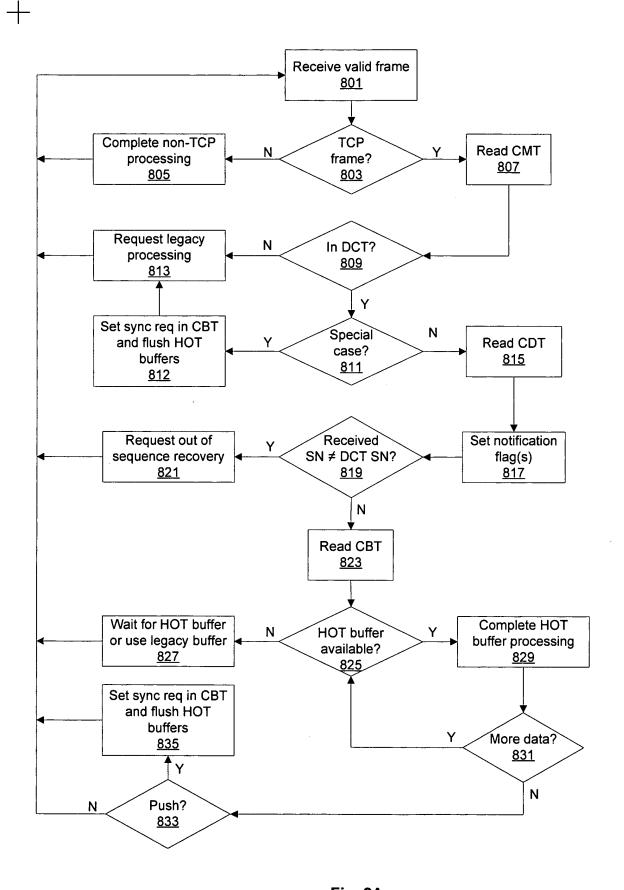


Fig. 8A

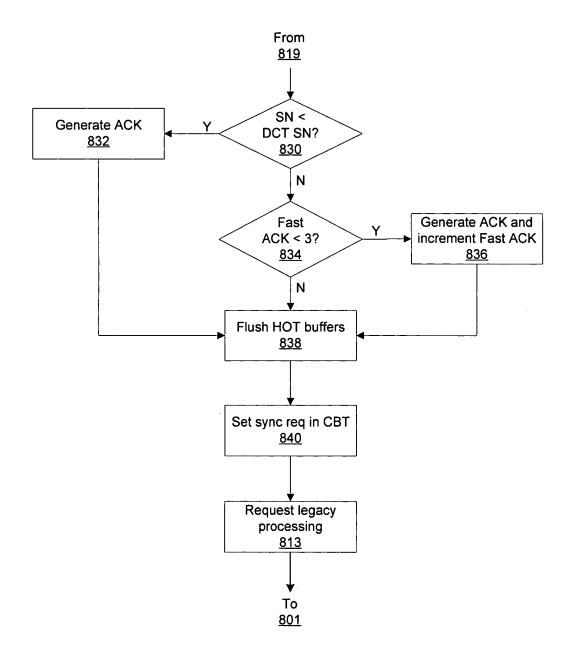


Fig. 8B

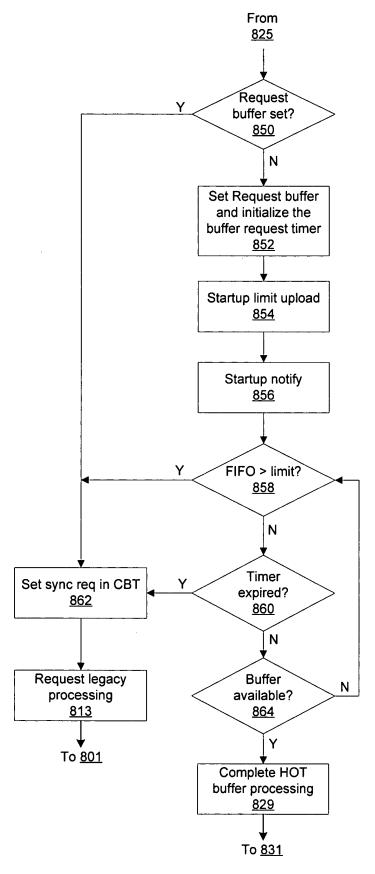


Fig. 8C

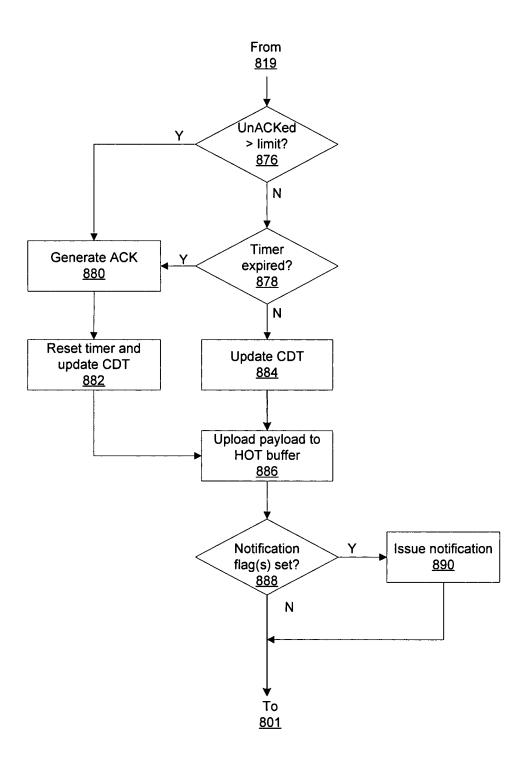


Fig. 8D

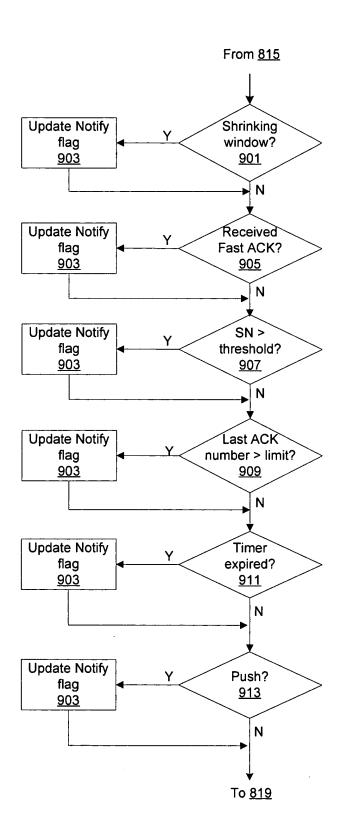


Fig. 9A

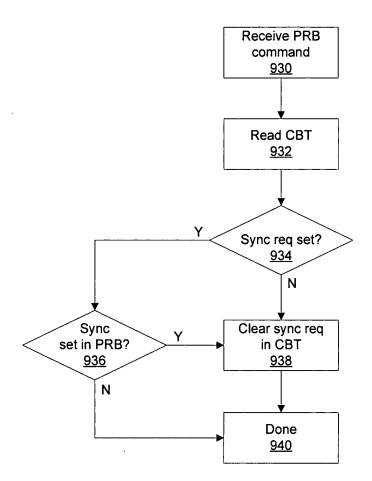


Fig. 9B

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1007	1005	1010
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Fig. 10A

1007 1015 1020 1025

1007	1015	1030	1035
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Fig. 10B

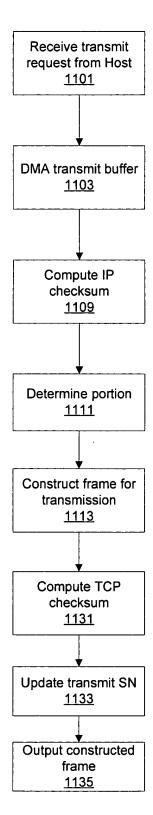


Fig. 11A

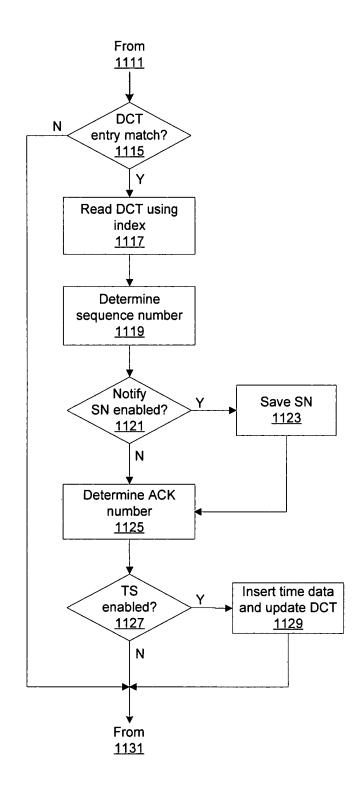


Fig. 11B

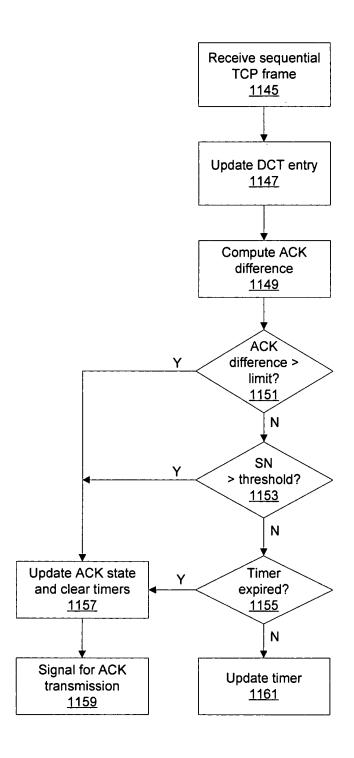


Fig. 11C